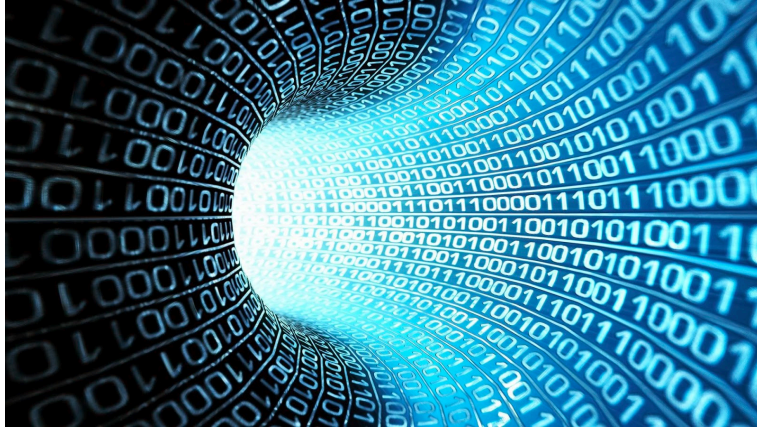
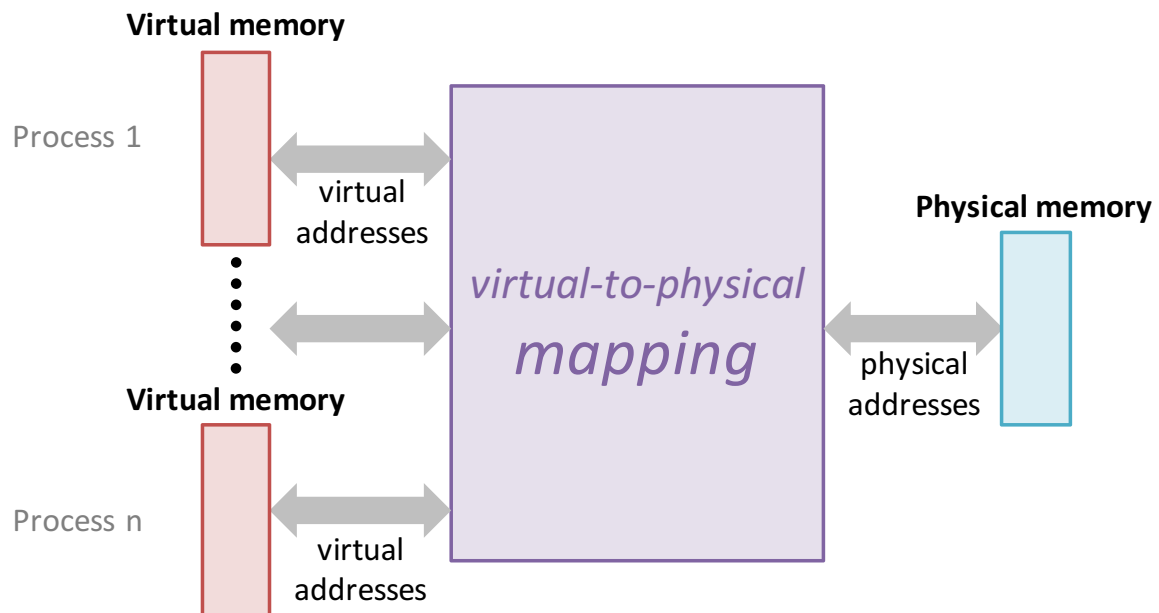


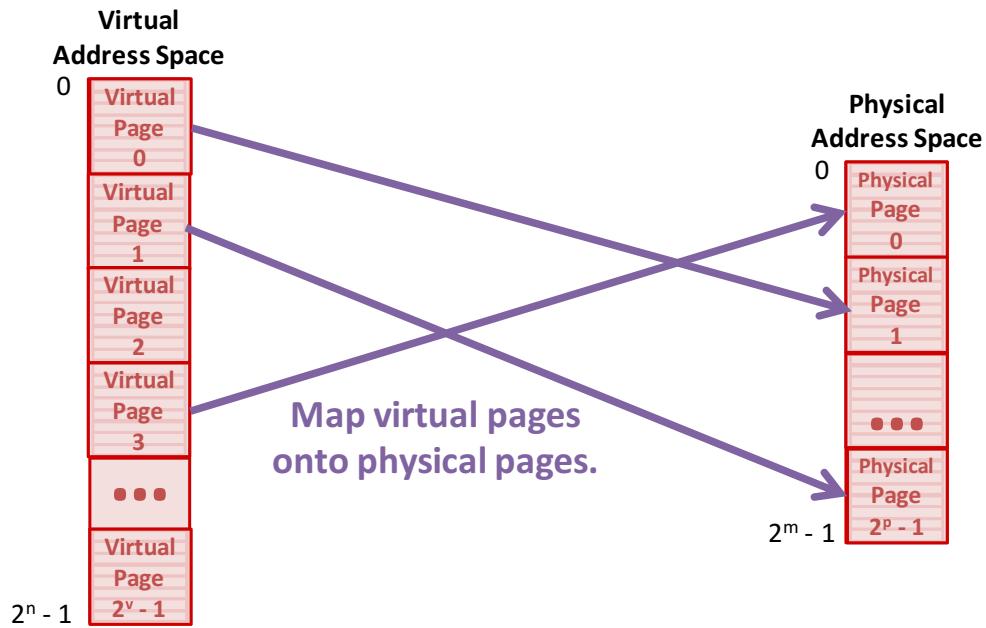
Virtual Memory (and wrap-up...)



Virtual and Physical Addresses

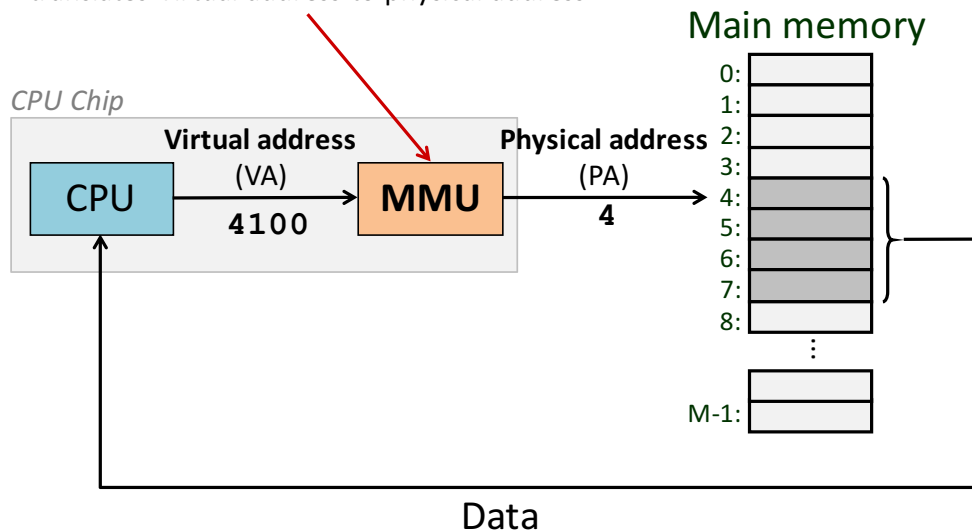


Memory Pages

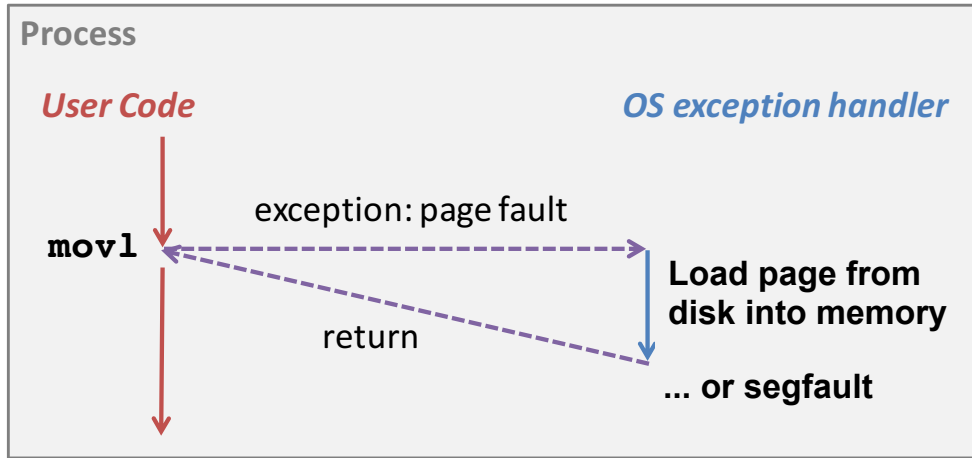


Address Translation

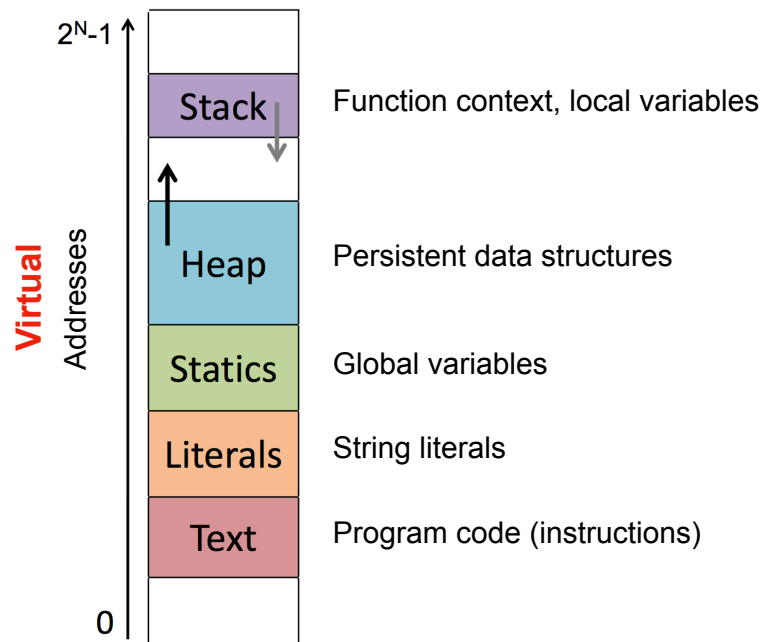
Memory Management Unit
translates virtual address to physical address



Page Fault



Memory Layout (redux)



Big Ideas in Systems

Abstraction

Representation

Performance

Security + Reliability

