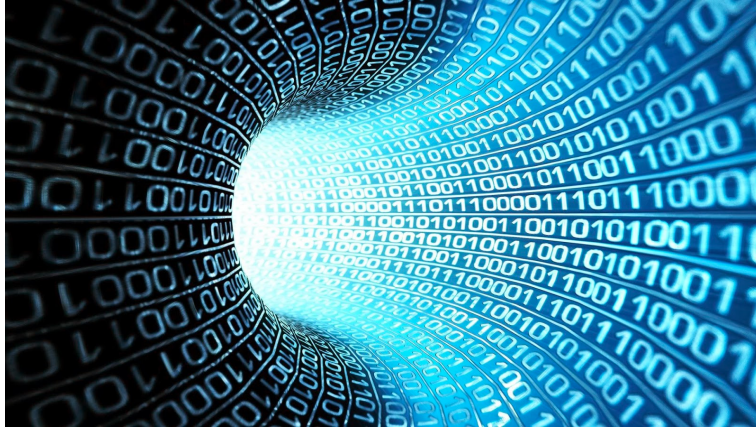
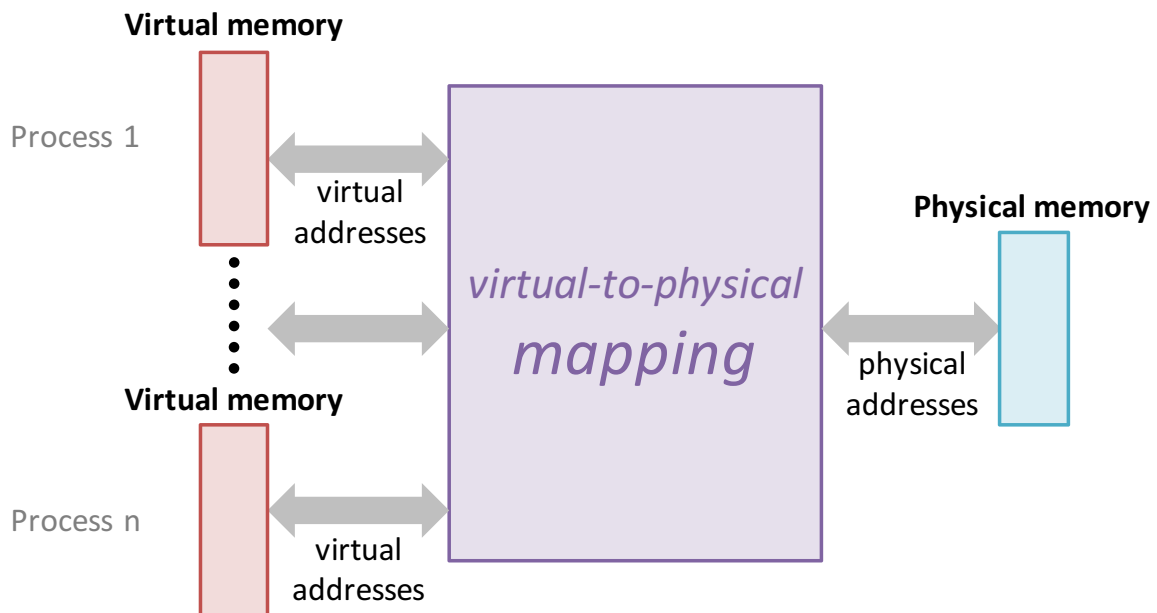


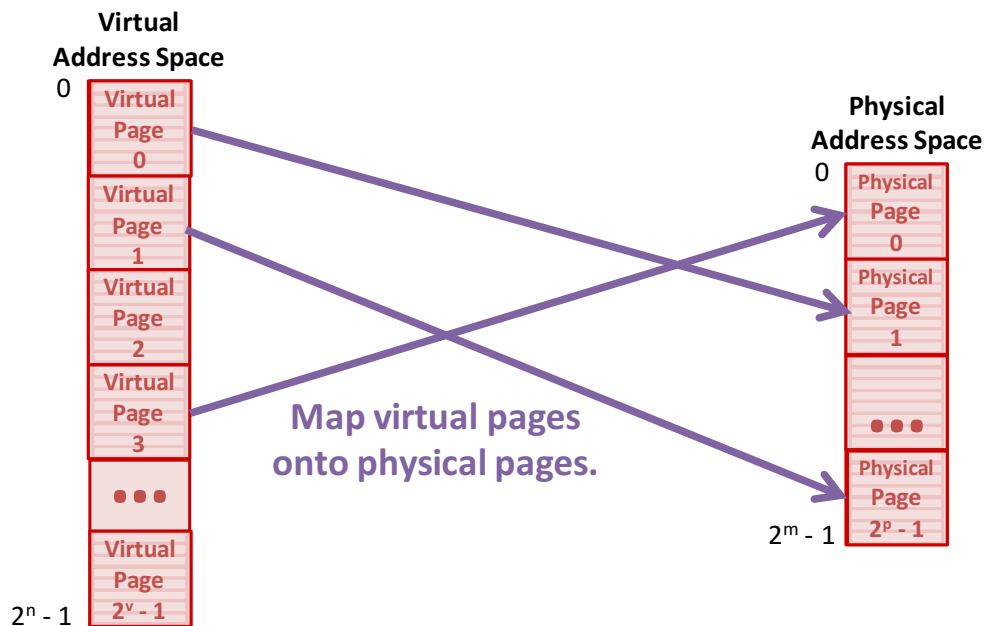
Virtual Memory (and a bit more...)



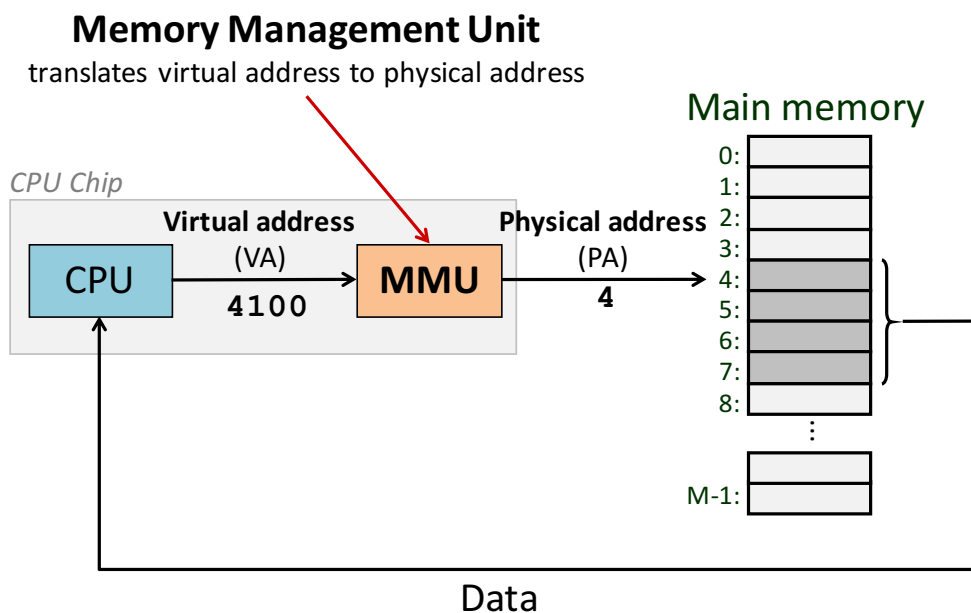
Virtual and Physical Addresses



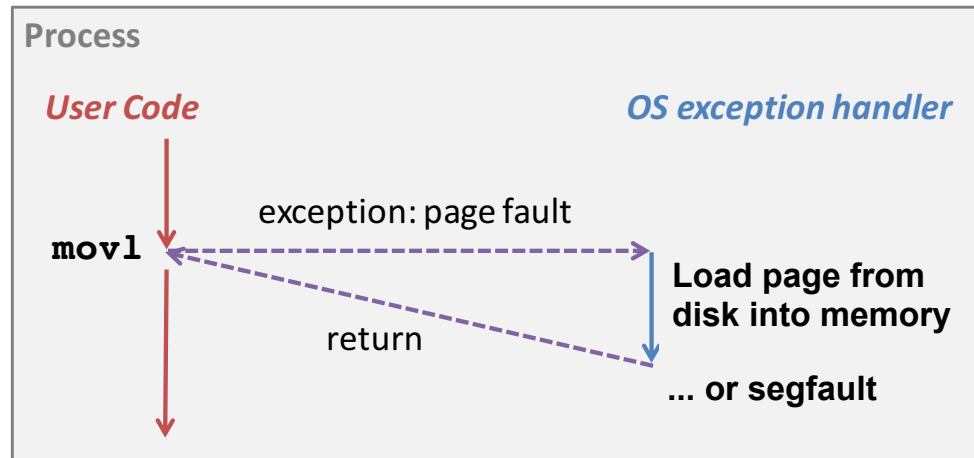
Memory Pages



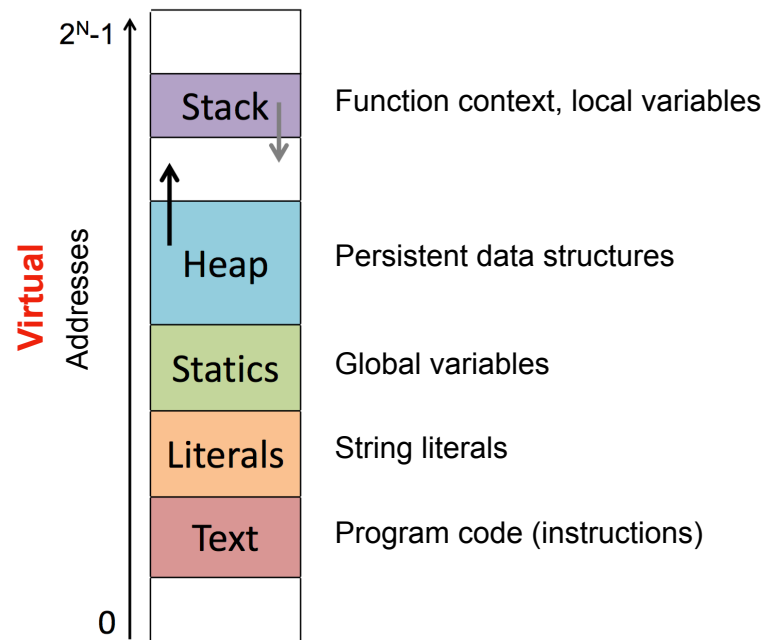
Address Translation



Page Fault



Memory Layout (redux)



Program Generation Workflow

