

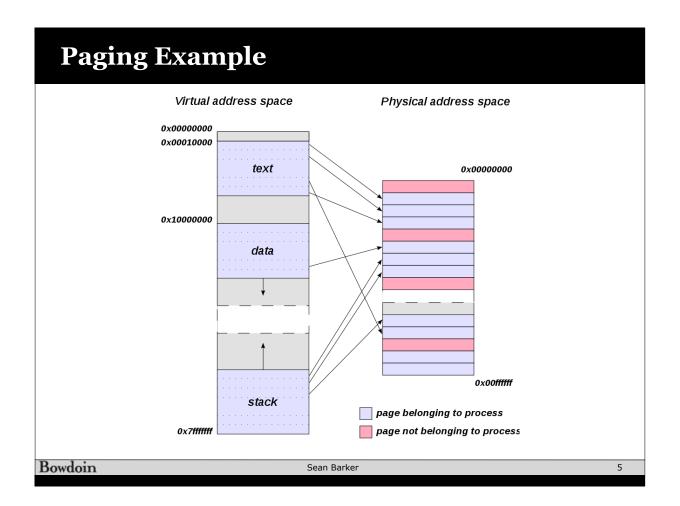
Address Translation Memory Management Unit translates virtual address to physical address Main memory CPU Chip Virtual address Physical address 2: (VA) (PA) **CPU MMU** 4: 4100 5: 6: 7: M-1: Data

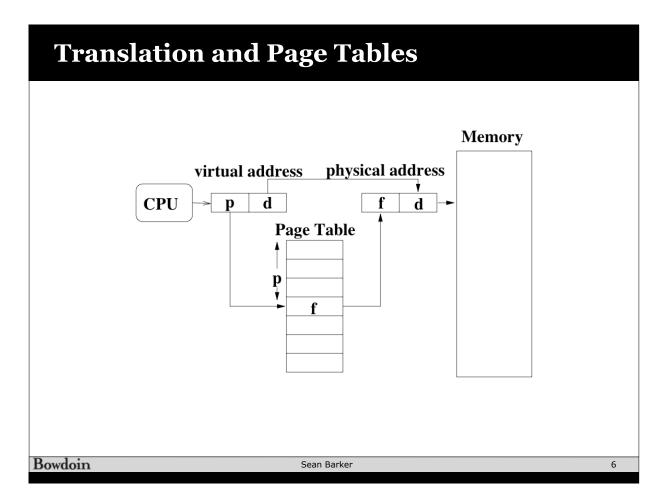
Sean Barker

3

Bowdoin

Process User Code exception: page fault mov1 ???? return Sean Barker 4





Translation Look-aside Buffer (TLB)

