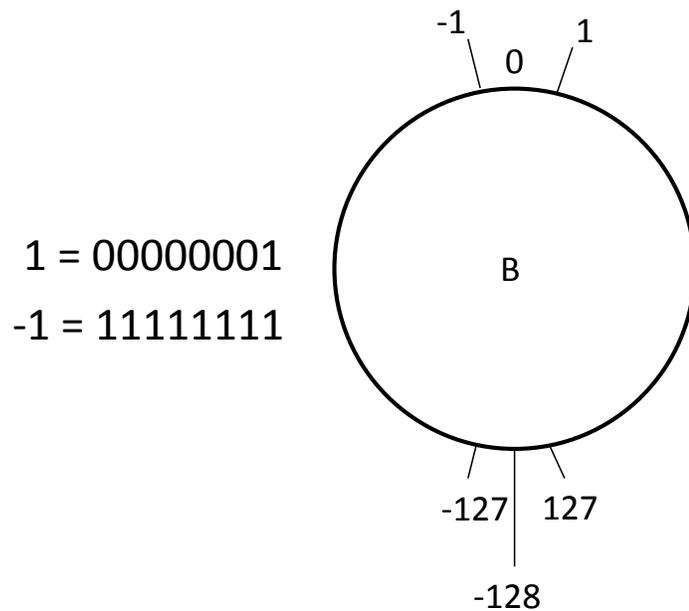


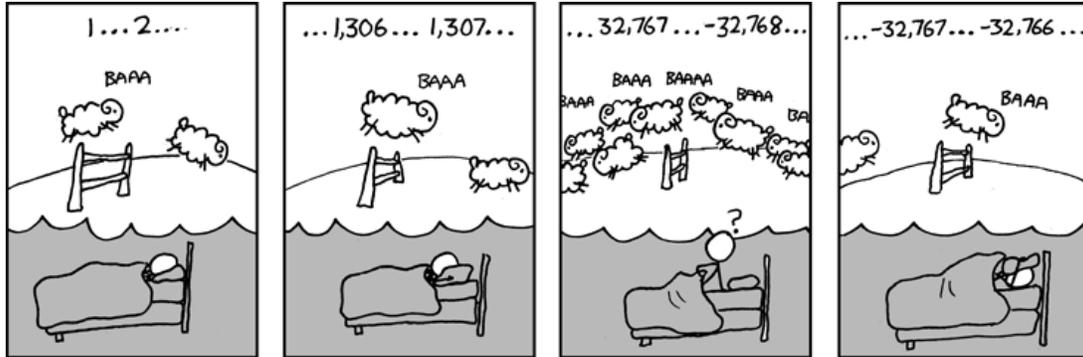
Two's Complement



Signed vs Unsigned

Bits	Signed		Unsigned
0000	0	↔ = ↔	0
0001	1		1
0010	2		2
0011	3		3
0100	4		4
0101	5		5
0110	6		6
0111	7		7
1000	-8	↔ +/- 16 ↔	8
1001	-7		9
1010	-6		10
1011	-5		11
1100	-4		12
1101	-3		13
1110	-2		14
1111	-1		15

Overflow



Overflow

